





Niagara Embedded Intel Powered Control and IoT Hardware













Elkhart Lake Platform Overview



What It Is

- Elkhart lake is the 2020 product offering in our Entry swimlane
- IOTG led product with gen-gen CPU and GFX performance over Apollo Lake. Real Time and Functional Safety capabilities.
- Optimized for industrial, retail, office automation, and transportation applications.
- IOTG Timeline: Production Intel® Pentium® and Celeron N and J Series Q1'21, Intel Atom® x6000E Series Q1'21

Outstanding Performance

- Gen-Gen Performance improvements up to 1.7x on CPU and up to 2x on graphics¹
- Up to 4Kp60 display resolution on three simultaneous displays

6

Improved I/O

- x12 Configurable HSIO lanes, PCle 3.0, SATA Gen 3, USB 3.1
- Integrated 2.5 Gigabit Ethernet
- Fully Integrated Voltage Regulator (FIVR)



- Intel® Programmable Services Engine
- Intel® Time Coordinated Computing and TSN for Real Time applications
- Functional safety: SIL2/Cat.3 PL d certified, SIL3/Cat.4 PL e capable

IOT Differentiation

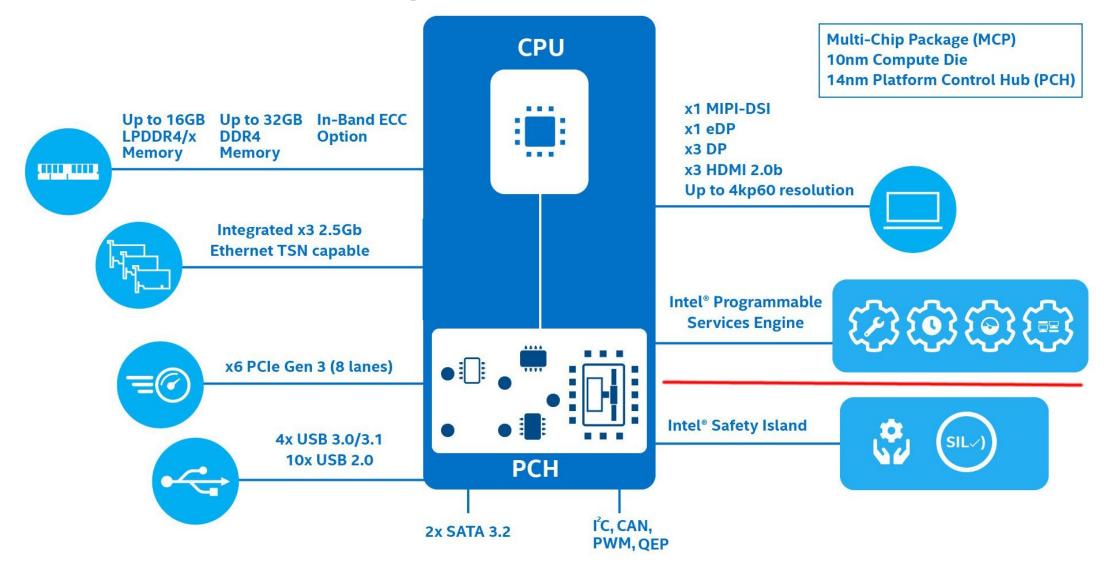
- Extended product availability², PC Client, Embedded & Industrial Use Conditions
- Extensive OS and firmware support: Windows* 10 IoT Enterprise, Yocto* Project*, Android*, Ubuntu* & Wind River Linux* LTS & VxWorks* via WRS UEFI, Coreboot, Slim Bootloader
- Tools/SDK: Media SDK, OpenVINO™, Intel® TCC Tools
- 1. 3DMark®1Spec® CPU2006* SPECint2006 Rate Max users UP (4) & SPECint2006 Rate 1 user tests of Intel® Pentium® J6425 projection vs. Intel Pentium J4205 with Windows* 10 turbo off 1* 3DMark® Score,
- 2. *Other names and brands may be claimed as the property of others.







Elkhart Lake - Designed for the world of the IoT









Gen to Gen Performance Improvements

SPECint_rate_base2006 (1 copy) Single Thread

Up 1 9x

Performance Improvement

VS Intel® Pentium® J4205

SPECint_rate_base2006 (n copy) Multi Thread

Up 1.9x

Performance Improvement

VS Intel® Pentium® J4205

3DMark11 Graphics

Up 2.7x

Performance Improvement

VS Intel® Pentium® J4205

Source: Intel. SPEC metric estimates based on Pre-Si projections. 3DMark11 estimates are based on Pre-Si projections.

See backup for configuration details. For more complete information about performance and benchmark results, visit www.intel.com/benchmarks.





Intel® Time Coordinated Computing (Intel® TCC) & Time-sensitive Networking (TSN) Software

INTEL® TCC AND TSN SOFTWARE ENABLING SPANS MULTIPLE LAYERS OF SOLUTION STACK	TSN REFERENCE SOFTWARE	INTEL® TCC SOFTWARE SINGLE ENTRY POINT FOR DEVELOPERS	3 RD PARTY ENABLED SW
Application	Samples and Documentation	Code Samples & Tools	
Middleware	OPC UA and OpenAvnu	Intel® TCC Tools	
Operating System	Standard Linux TSN Interfaces	Intel® TCC Configurations, Yocto*	VxWorks*
Drivers	Linux preempt-RT	Optimized for real-time and time synchronization	
BIOS / Firmware	Standard BIOS	Intel® TCC Settings	ACRN*, RTS*
Hardware Platform	Integrated/Discrete Ethernet w/TSN	Intel® TCC Processor Features	
Developer Language	С	С	





Elkhart Lake Key Target Verticals and

Application





Intel® Programmable

Services Engine

Features





eBMS/IoT-Controller Capabilities

- Familiar Package Standards
- OEM Control Applications
- Edge Analytics
- Full Throttle Niagara Web Ul
- Industrial Applications with ACE
- Future Proof Power





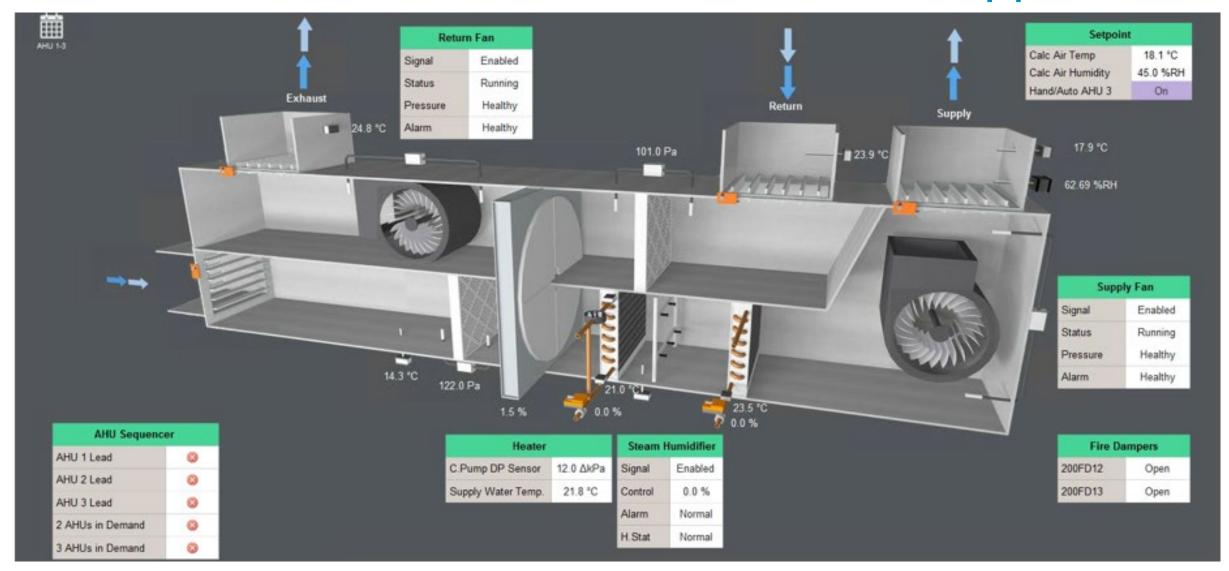
Unrivalled Power

- Dual core 1.3 GHz Fanless
- Quad core 1.5 GHz Fanless
- 16GB DDR RAM
- 32GB eMMC





eBMS/IoT-Controller Foundation Control Application







Extensive I/O and Ports

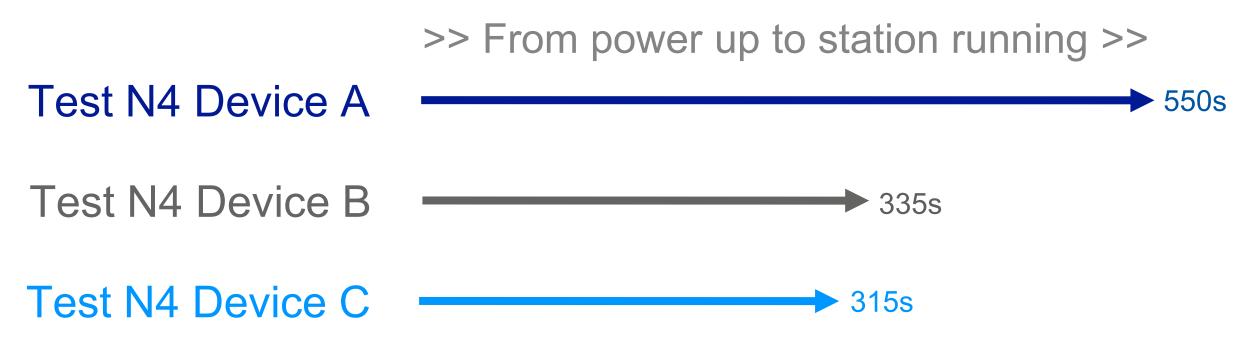
- 8 Universal Outputs / 4 230VAC Relays
- 6 x Universal Inputs / 6 Digital Inputs
- 2 x Ethernet Ports / 2 x USB3.0 Ports
- 3 x RS485 Ports / 1 x RS232 Port
- Edge IO Connector







Performance Benchmarking











Collaboration for Growth

















Thank You and get in touch today

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